Event Name:- Quick Circuit Making

Name of Staff Coordinator:-

1. Prof. S.S.Dubal (BSIOTR)
2. Prof. K.D.Pawar(ICOER)

Name of Student Coordinator:-

1. Kranti Mohite (8380996580)
2. Mukesh Mundhe(8007003084)

Venue Of Event:- E&TC Department, BSIOTR
Entry Fee:- 150/- per group
No. of Participants:- 2 Max.

Prize :- 1st :- 2000/-
2nd :- 1000/-

Round :-

1. **Multiple Choice Questions: 25 Questions in 30 minutes**
2. **Questions on Circuit Debugging: 10 Questions in 15 minutes**
3. **Build maximum given Analog/Digital circuits in 30 minutes**

Event Rules and Regulations:-

Round 1

1. 25 Number of objective questions are to be solved within 30 min.
2. Objective questions will be based on subjects like Basic Electronics and Basic Electrical. Electronic Devices and Circuits(EDC), Digital Logic Circuit(DID), Linear Integrated Circuits(LIC) etc.
3. Use of Scientific Calculator is allowed.
4. Make a Tick Mark on appropriate choice.
5. Once tick mark is done on the choice then it cannot be altered. For multiple tick marks zero marks will be given.
6. If book or any other supporting material is found then it would be treated as unfair means and that group will be disqualified from the competition.
7. Results of 1st Round will he displayed on Notice Board.
8. After evaluation selected groups will appear for second round.

Round 2 :- Questions on Circuit Debugging

1. 10 number of questions are to be solved within 15 minutes.
2. Maximum errors are to be found for each electronic circuit.
3. Result of 2nd Round will be displayed on notice board.
4. After evaluation selected groups will appear for 3rd Round.

Round 3 :- Circuit design and implementation

1. Problem statements will be given for circuit designing and implementation to each group.
2. On completion, the final circuit should work for all the test cases.
3. On completion of first circuit implementation, Group may implement another circuit within prescribed time (30 min). Number of circuits implemented will also be the criteria for selection.
4. It will also be judged on the basis of circuit simplicity, efficiency and ability to fulfill criteria given in problem statement.
5. In case if there is a of tie, judging will be done on the methodology followed to achieve the solution to the problem. The decision of the judge will be final and binding.
6. In case of complete solution, it shall be judged on the method of approach and closeness to real solution.
7. The verdict of the respected judge shall be final and binding.